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PATENT NUMBER and
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U.S. UTILITY Patent Application

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APPL NUM 10049971	FILING DATE 02/12/2002	CLASS 423	SUBCLASS 17	GAU 1754	EXAMINER 11509
**APPLICANTS: Komiya Satoshi; Yoshino Shiro; Danbata Masayoshi; Hayashida Kouichirou; <div style="text-align: center;">117</div>					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/JP00/04216 06/26/2000 (M)					
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** FOREIGN APPLICATIONS VERIFIED: JAPAN 241187/1999 08/27/1999 (M)					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials (M)				ATTORNEY DOCKET NO 86396	
TITLE : Epitaxial silicon wafer				<small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)</small>	

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
		DRAWING	
		Sheets Drwg.	Figs. Drwg.
		Print Fig.	
		Applicati n Examiner	
ISSUE FEE Amount Due Date Paid		Assistant Examiner Primary Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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